

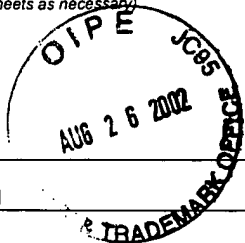
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Sheet 1 of 1

Complete if Known

Application Number	09/551027
Filing Date	April 17, 2000
First Named Inventor	Noble Jr., Wendell
Group Art Unit	2822
Examiner Name	Trinh, Michael

Attorney Docket No: 00303.379US2

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
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MT	US-5396093	03/07/1995	Lu, C.	257	306	08/12/1994
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Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
MT	JP-2000-164883	06/16/2000	Yamazaki, S., et al	H01L	29/786	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
MT		RHYNE, In: Fundamentals of Digital Systems Design, Prentice Hall, New Jersey, (1973),pg. 70-71	
MT		SU, D., et al. , "Experimental Results and Modeling Techniques for Substrate Noise in Mixed Signal Integrated Circuits", IEEE Journal of Solid State Circuits, 28(4), (1993),pp. 420-430	

EXAMINER

Michael Trinh

DATE CONSIDERED

9/16/02

Substitute Disclosure Statement Form (PTO-1449)

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